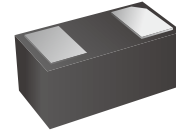


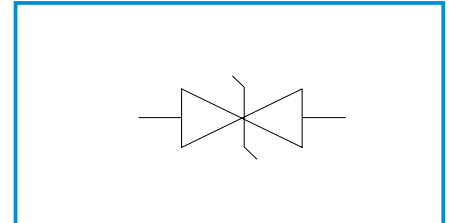
## 1-Line Ultra Low Capacitance Bi-directional TVS Diode

### Features

- Ultra small package: 1.0x0.6x0.5mm
- Ultra low capacitance: 0.3pF typical
- Ultra low leakage: nA level
- Operating voltage: 5V
- Low clamping voltage
- AEC-Q101 qualified
- Complies with following standards:
  - IEC 61000-4-2 (ESD) immunity test
    - Air discharge:  $\pm 25\text{kV}$
    - Contact discharge:  $\pm 22\text{kV}$
  - IEC61000-4-5 (Lightning) 4A (8/20 $\mu\text{s}$ )
- RoHS Compliant
- Marking: 21



Functional Diagram



### Applications

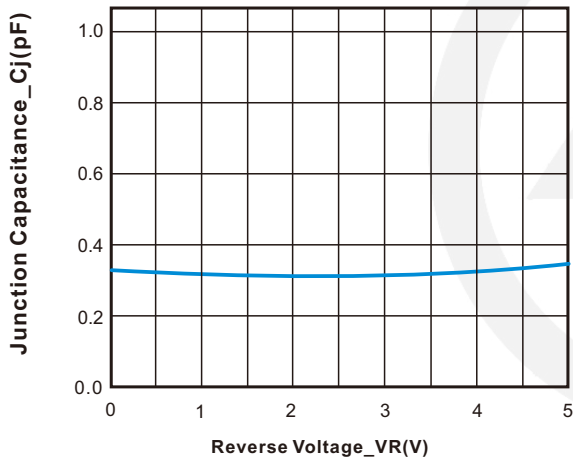
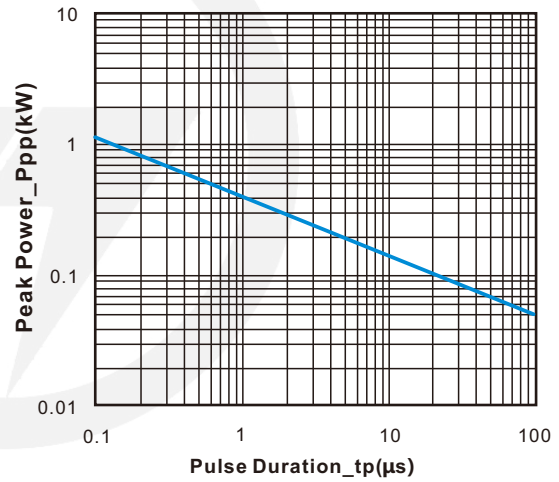
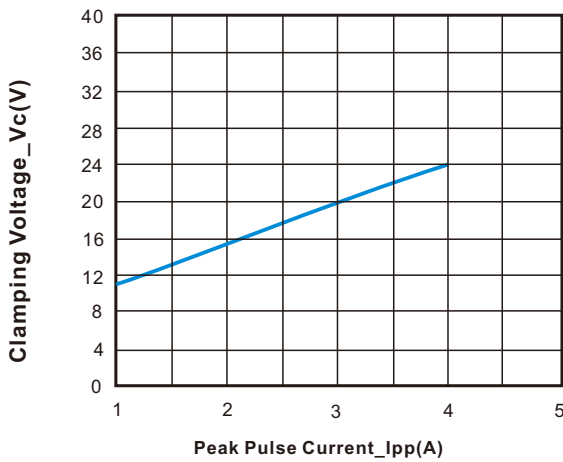
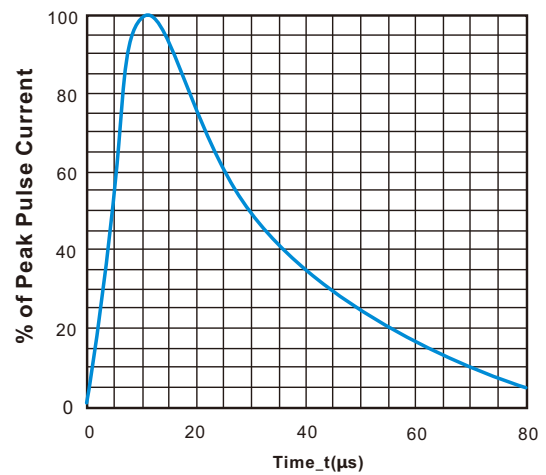
- Cellular Handsets and Accessories
- Display Ports
- MDDI Ports
- USB Ports
- Digital Visual Interface (DVI)
- PCI Express and Serial SATA Ports

### Absolute Maximum Ratings (T<sub>amb</sub>=25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20 $\mu\text{s}$ )	P <sub>pk</sub>	100	W
Peak Pulse Current (8/20 $\mu\text{s}$ )	I <sub>PP</sub>	4	A
ESD per IEC 61000-4-2 (Air)	V <sub>ESD</sub>	$\pm 25$	kV
ESD per IEC 61000-4-2 (Contact)		$\pm 22$	
Operating Temperature Range	T <sub>J</sub>	-55 to +125	°C
Storage Temperature Range	T <sub>stg</sub>	-55 to +150	°C

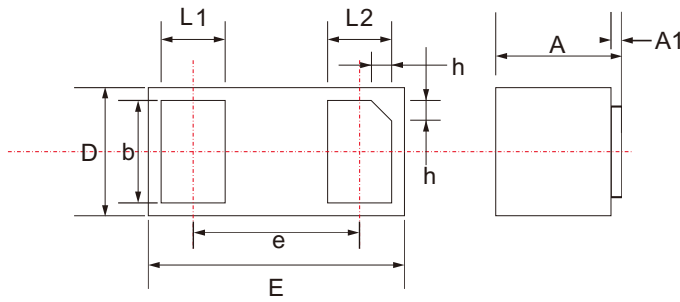
**Electrical Characteristics** (TA=25°C unless otherwise specified)

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Working Voltage	VRWM			5	V	
Breakdown Voltage	VBR	6.5		9.5	V	IT = 1mA
Reverse Leakage Current	IR			0.2	uA	VRWM = 5V
Clamping Voltage	VC			12	V	I <sub>PP</sub> = 1A (8 x 20μs pulse)
Clamping Voltage	VC			25	V	I <sub>PP</sub> = 4A (8 x 20μs pulse)
Junction Capacitance	CJ		0.3	0.5	pF	VR = 0V, f = 1MHz

**Characteristic Curves**

**Junction Capacitance vs. Reverse Volatage**

**Peak Pulse Power vs. Pulse Time**

**Clamping Voltage vs. Peak Pulse Current (tp=8/20μs)**

**8x20μs Pulse Waveform**

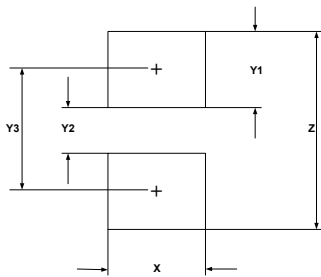
## DFN-1006 Package Outline

Unit: mm



SYMBOL	DIMENSIONS	
	MIN.	MAX.
D	0.550	0.650
E	0.950	1.050
L1	0.200	0.300
L2	0.200	0.300
b	0.450	0.550
e	0.650 TYP.	
A	0.450	0.550
A1	0.000	0.050
h	0.070	0.170

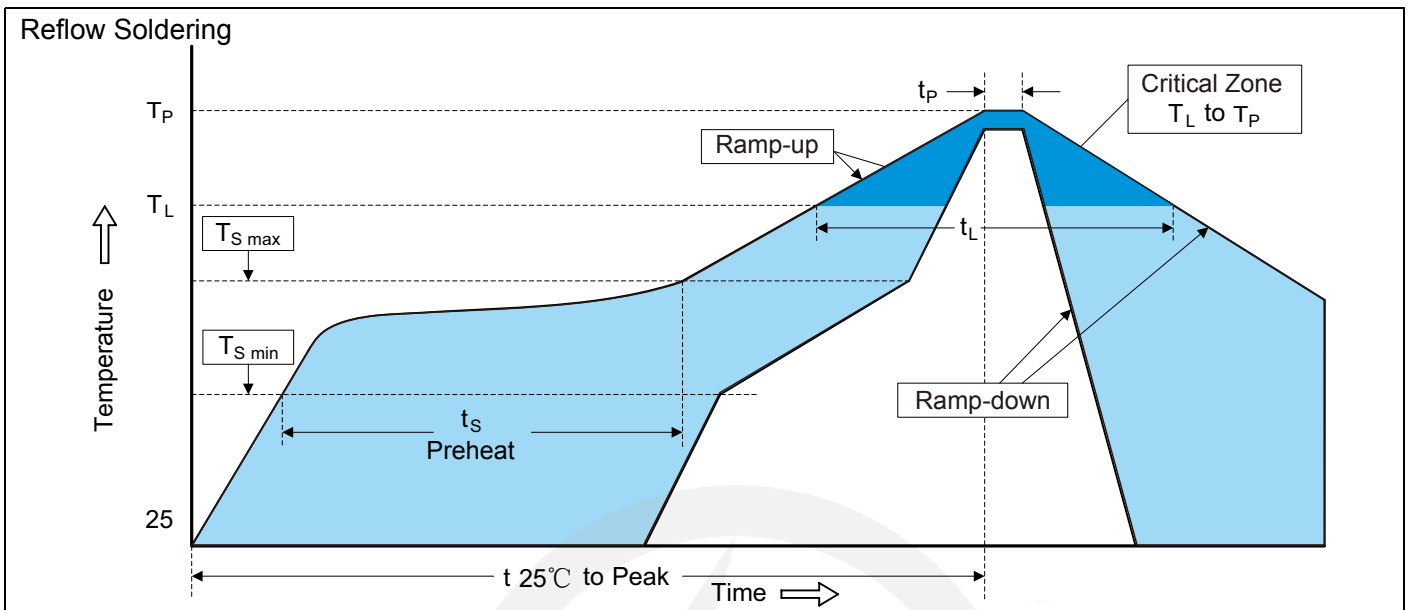
## DFN-1006 Suggested Pad Layout



SYM	DIMENSIONS
	MILLIMETERS
X	0.60
Y1	0.50
Y2	0.30
Y3	0.80
Z	1.30

Note:

1. Controlling dimension: in millimeters.
2. General tolerance:  $\pm 0.05\text{mm}$
3. The pad layout is for reference purpose only.

**Recommended Soldering Conditions**

**Recommended Conditions**

Profile Feature	Pb-Free Assembly
Average ramp-up rate ( $T_L$ to $T_P$ )	3°C/second max.
Preheat -Temperature Min ( $T_{S\ min}$ ) -Temperature Max ( $T_{S\ max}$ ) -Time (min to max) ( $t_s$ )	150°C 200°C 60-180 seconds
$T_{S\ max}$ to $T_L$ -Ramp-up Rate	3°C/second max.
Time maintained above: -Temperature ( $T_L$ ) -Time ( $t_L$ )	217°C 60-150 seconds
Peak Temperature ( $T_P$ )	260°C
Time within 5°C of actual Peak Temperature ( $t_P$ )	20-40 seconds
Ramp-down Rate	6°C/second max.
Time 25°C to Peak Temperature	8 minutes max.

**7" Reel**


D2	$\Phi 178.0 \pm 2.0$
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D3	$\Phi 50.0 \text{ Min.}$
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D4	$\Phi 13.0 \pm 0.5$
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W1	$16.0 \pm 2.0$
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Quantity: 10000PCS